

# REPLACEMENT SHEET

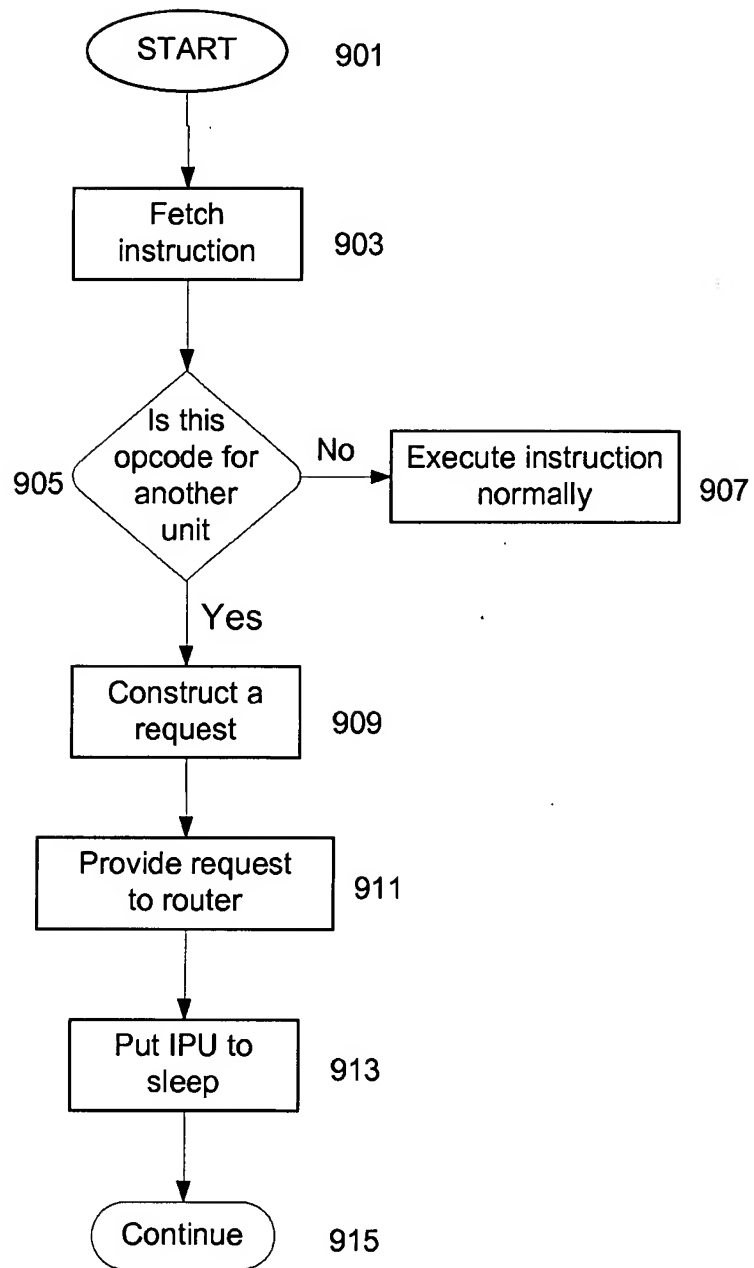


FIG. 9

REPLACEMENT SHEET

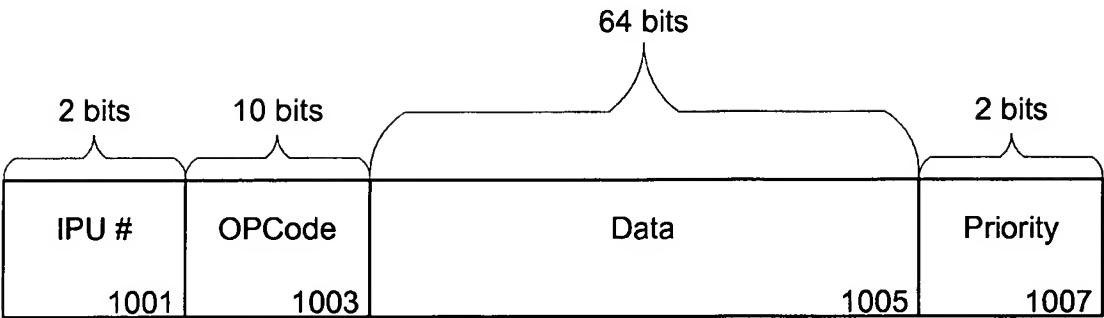


FIG. 10

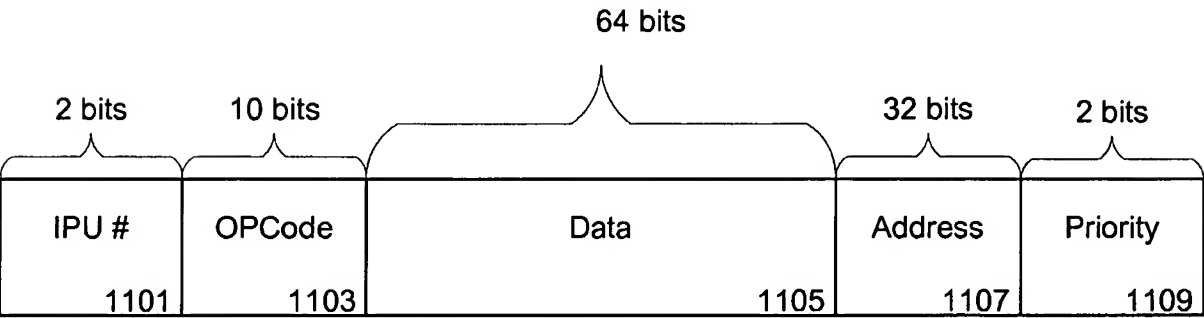
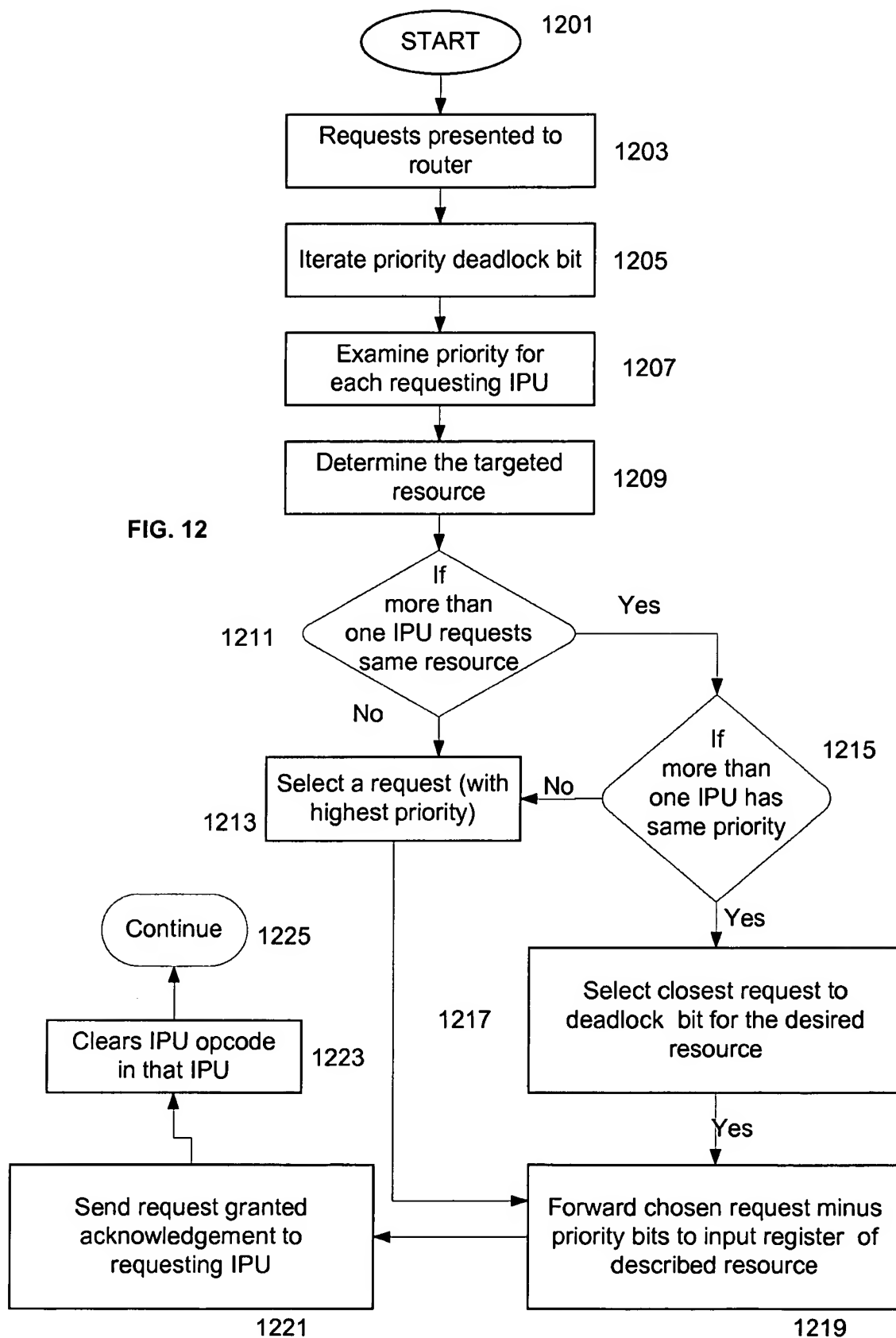


FIG. 11

# REPLACEMENT SHEET



# REPLACEMENT SHEET

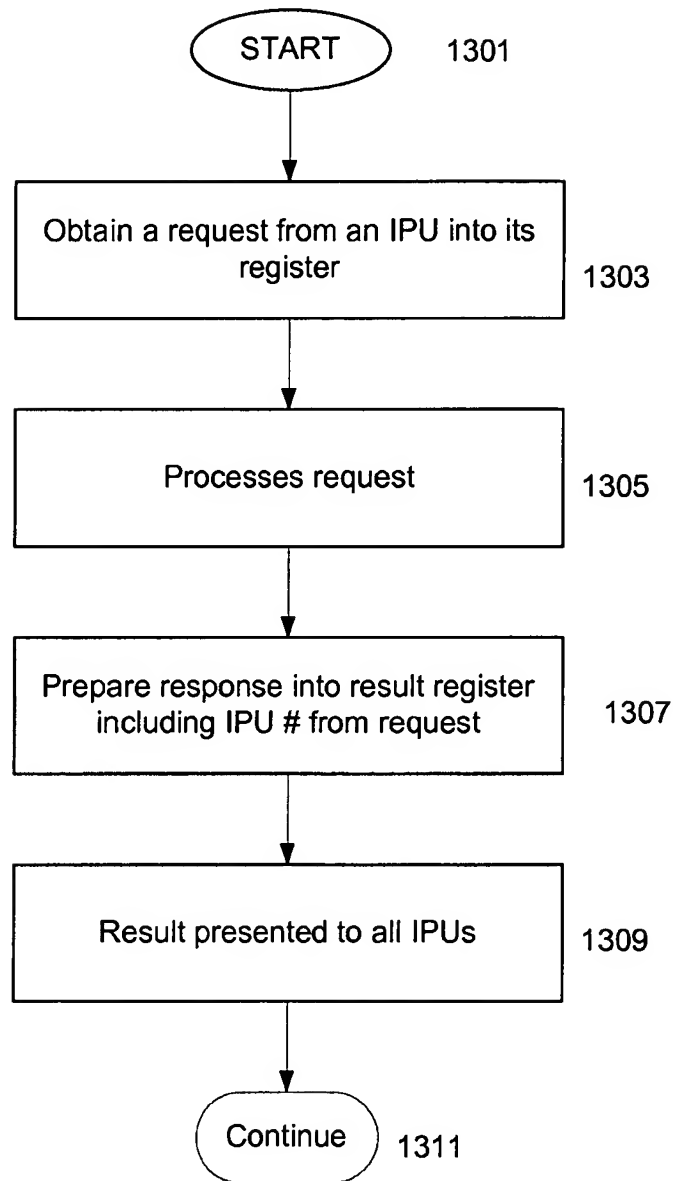


FIG. 13

# REPLACEMENT SHEET

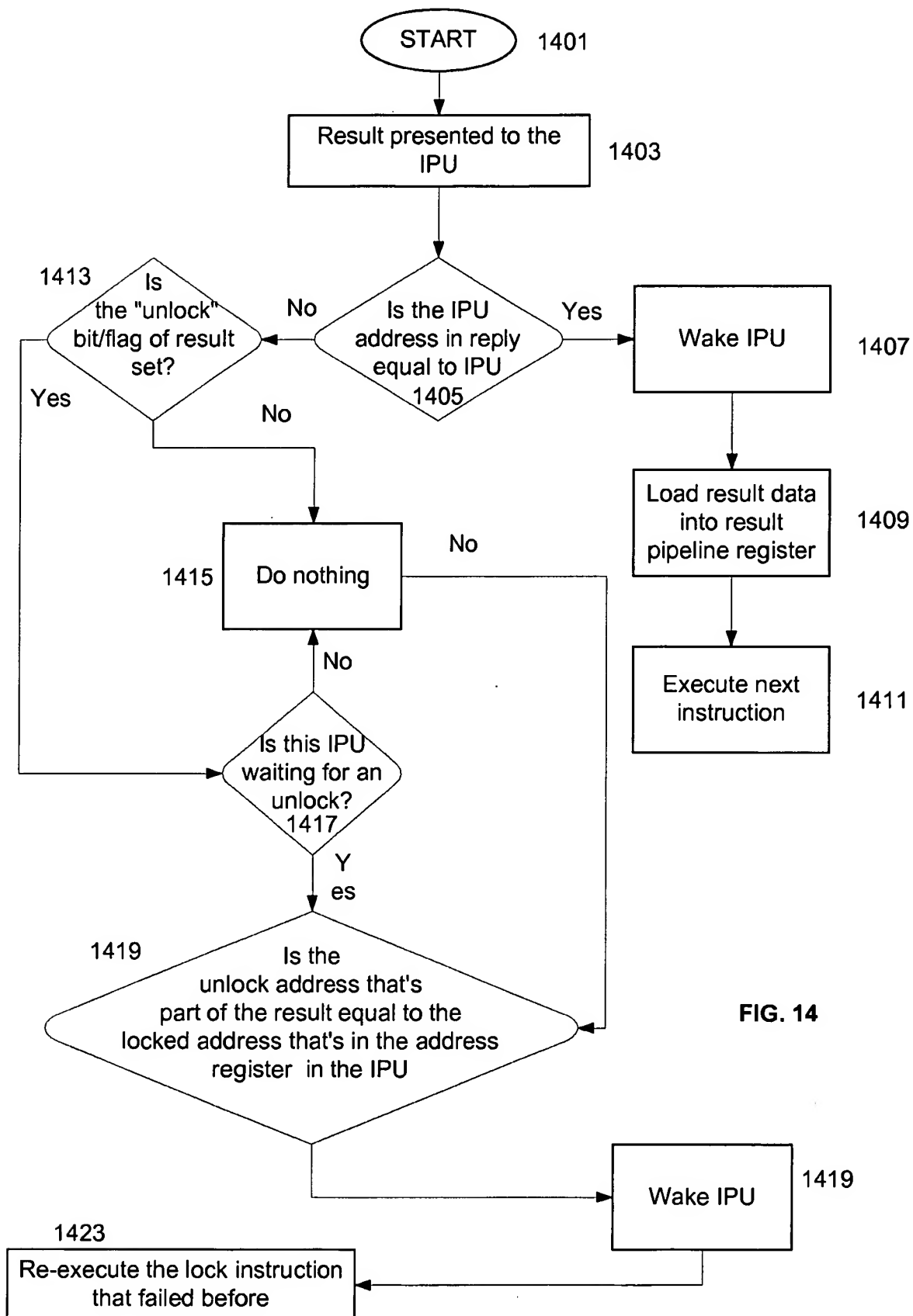
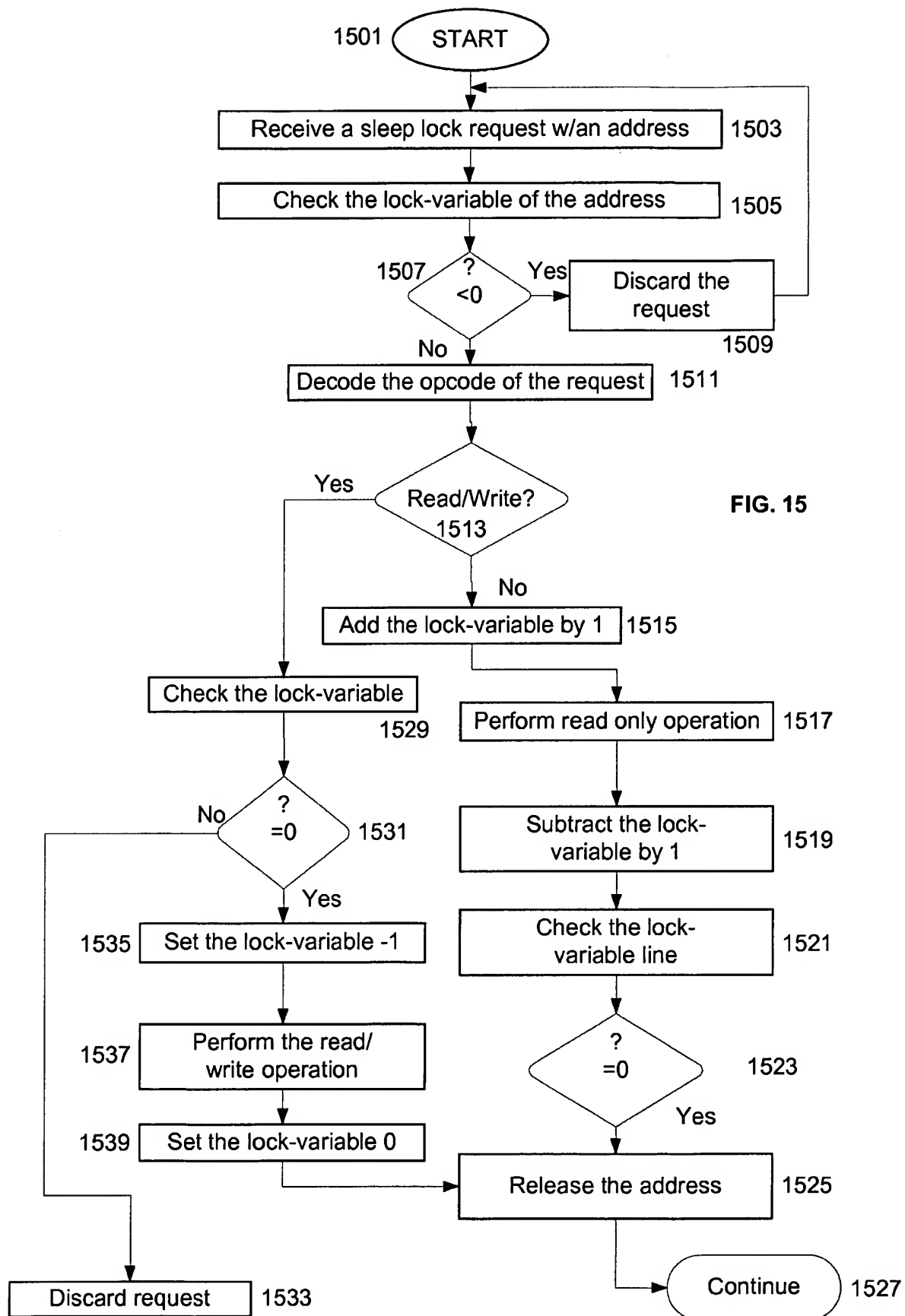


FIG. 14

# REPLACEMENT SHEET



# REPLACEMENT SHEET

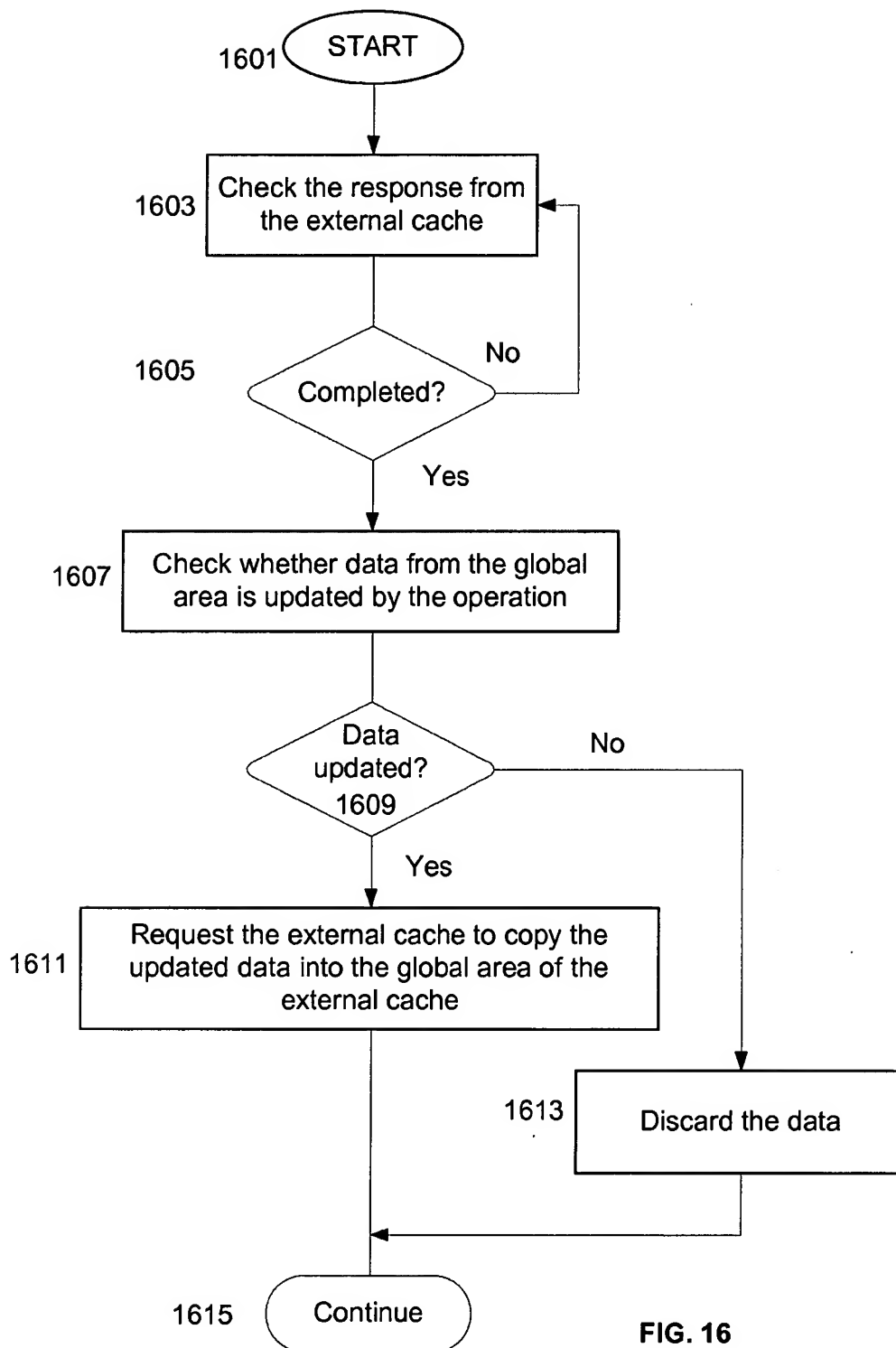


FIG. 16

# REPLACEMENT SHEET

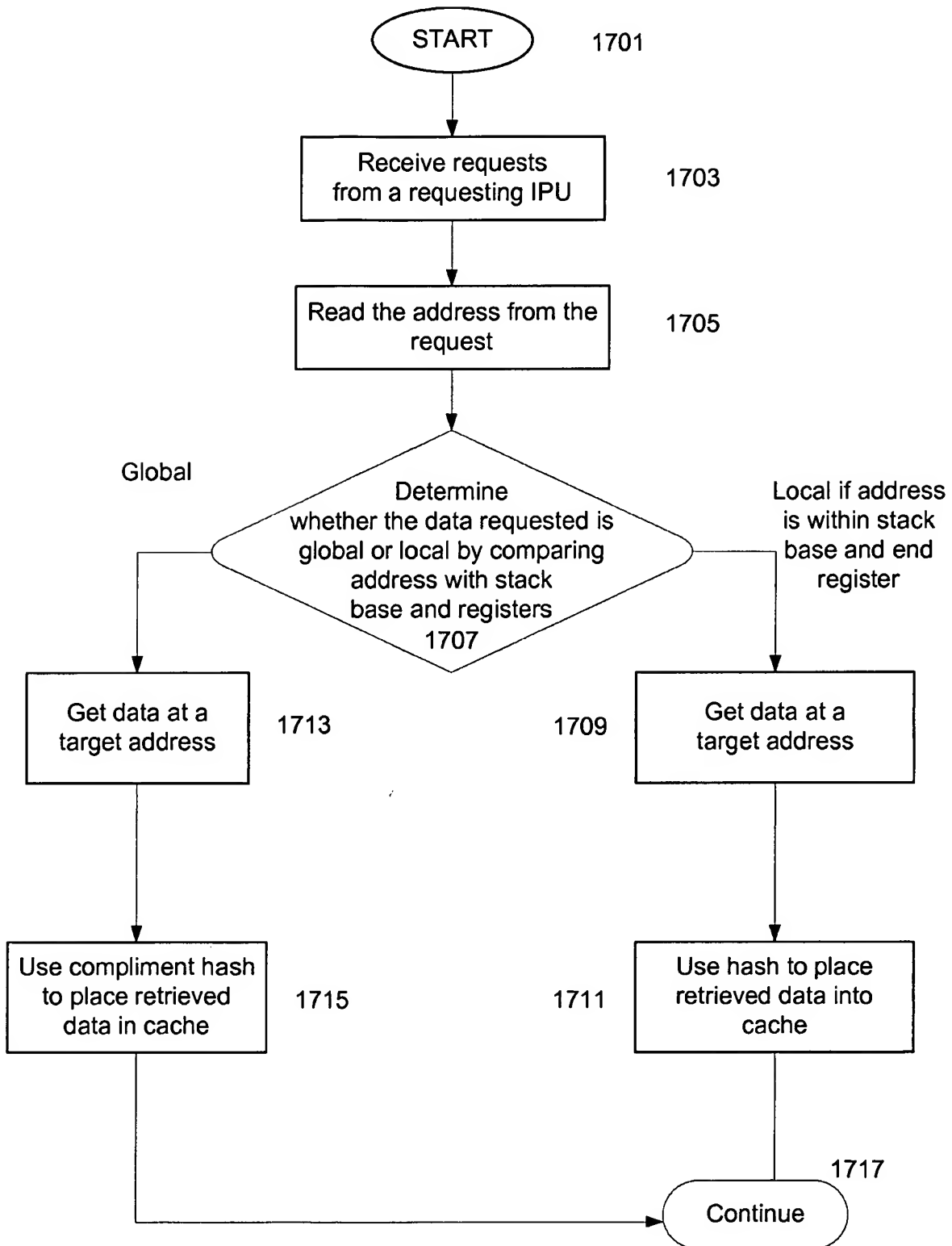


FIG. 17



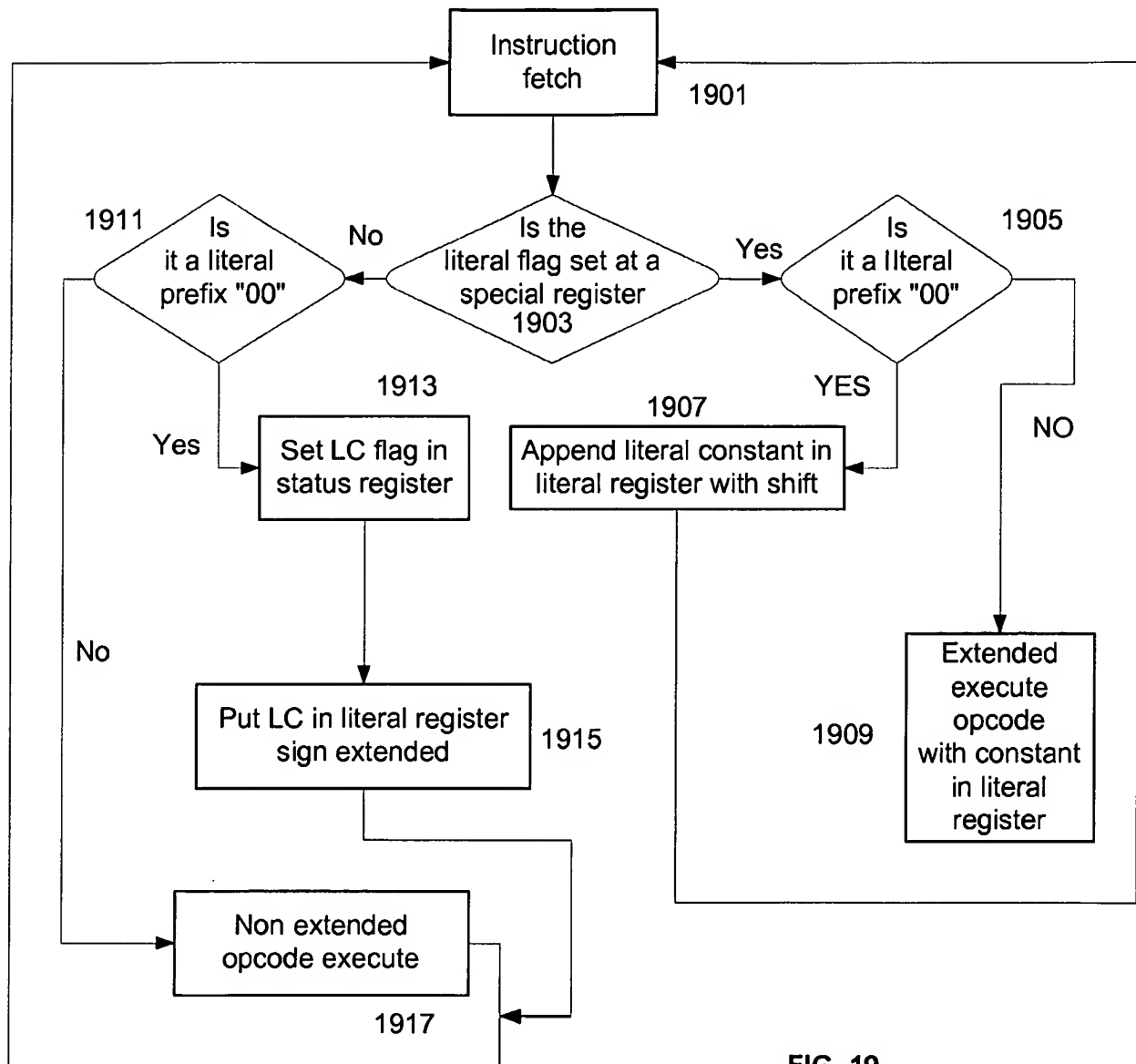


FIG. 19

## REPLACEMENT SHEET

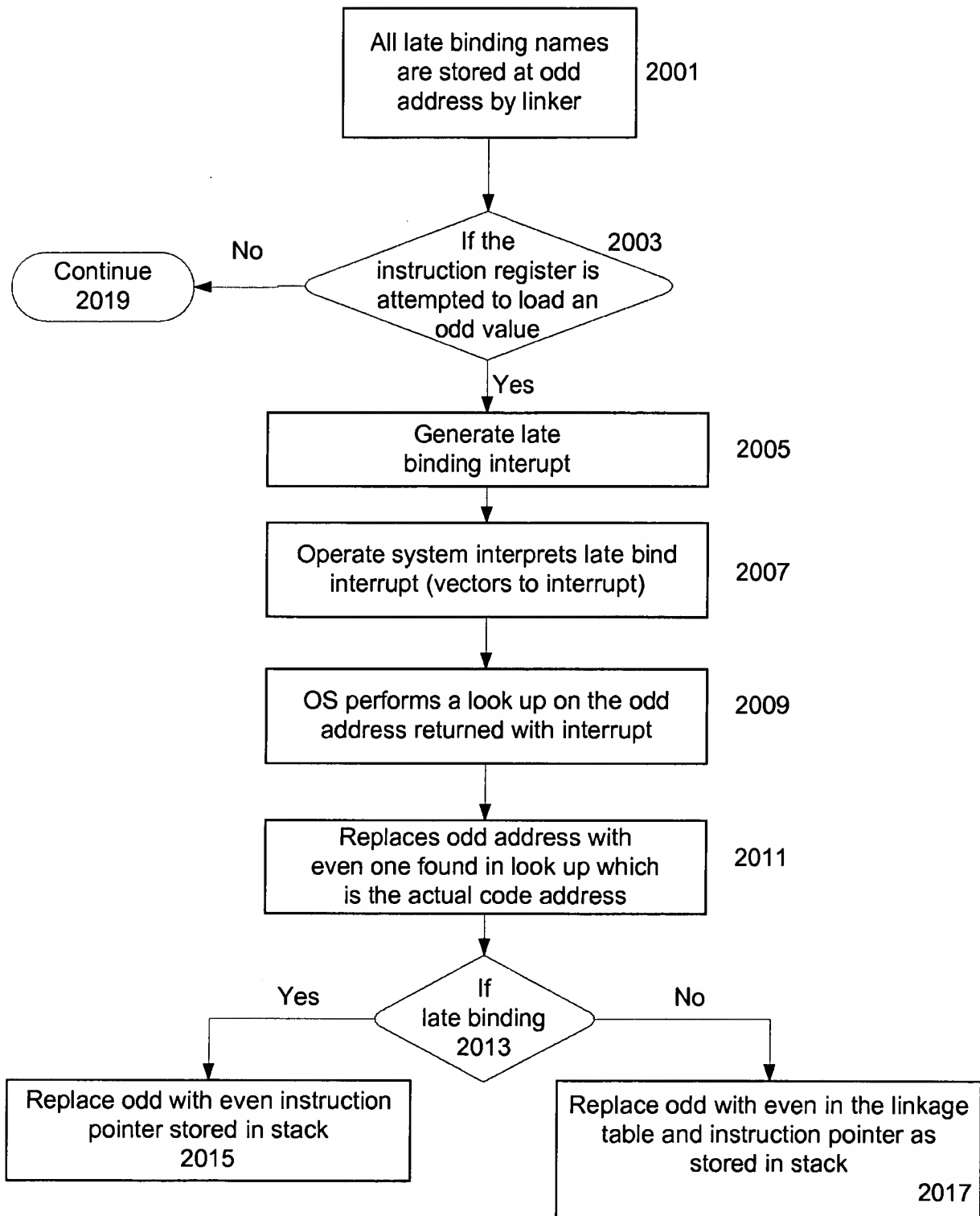


FIG. 20

## REPLACEMENT SHEET

### Status Register

Cycle Flags	Operations requesting number of cycles
0 (2101)	Normal (simple cycle) (2109)
1 (2103)	Double (two cycles/double processes load/save) (2111)
2 (2105)	Extend (multi cycles/double processes load/save) (2113)
3 (2107)	Repeated (multiple cycles/single/double process status) (2115)

FIG. 21

Type Flag	Number of Registers	Number of Bits	Type
0 (2201)	4 (2205)	32 (2209)	Data (2213)
1 (2203)	8 (2207)	32 (2211)	Address (2215)

FIG. 22

# REPLACEMENT SHEET

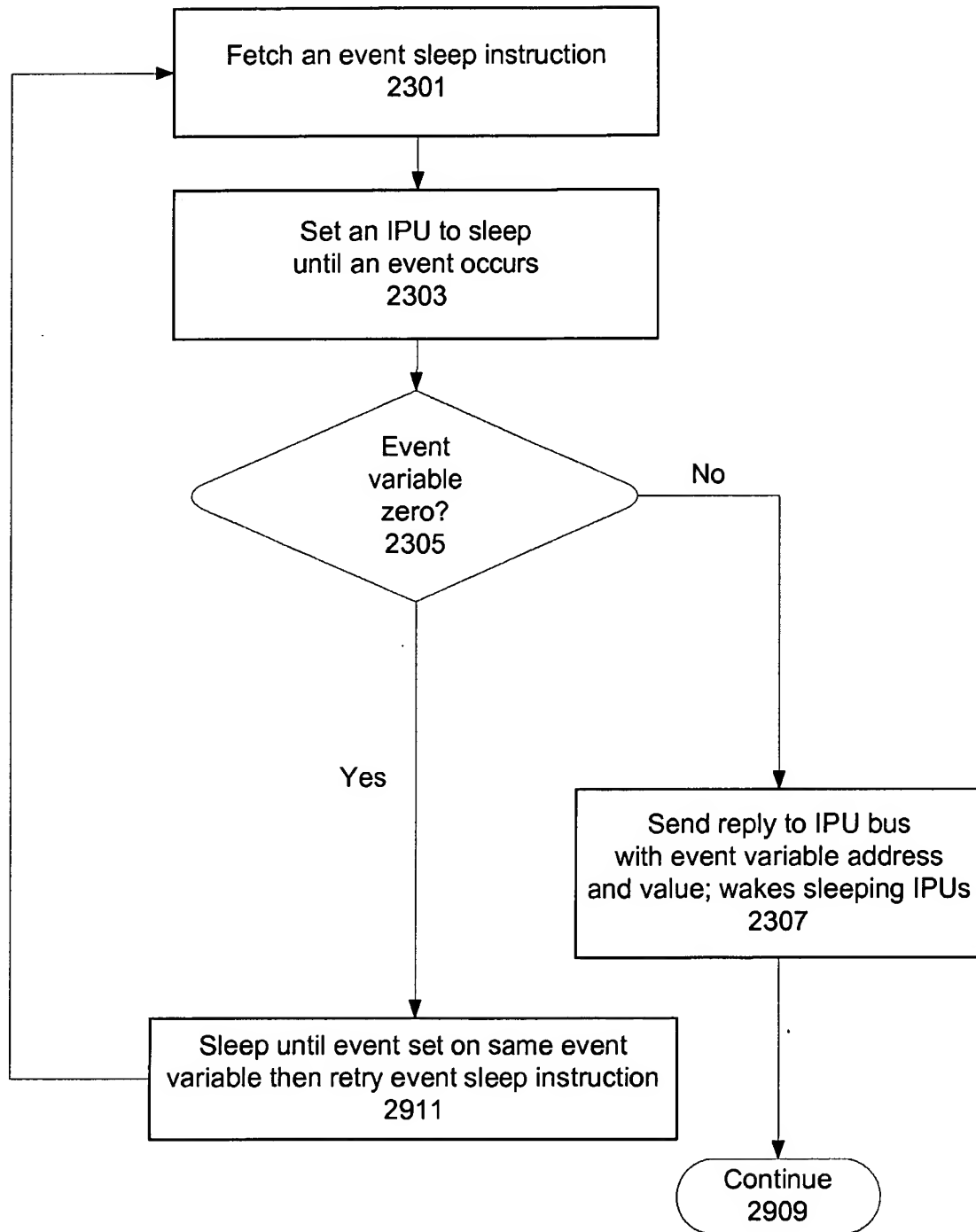


FIG. 23

REPLACEMENT SHEET

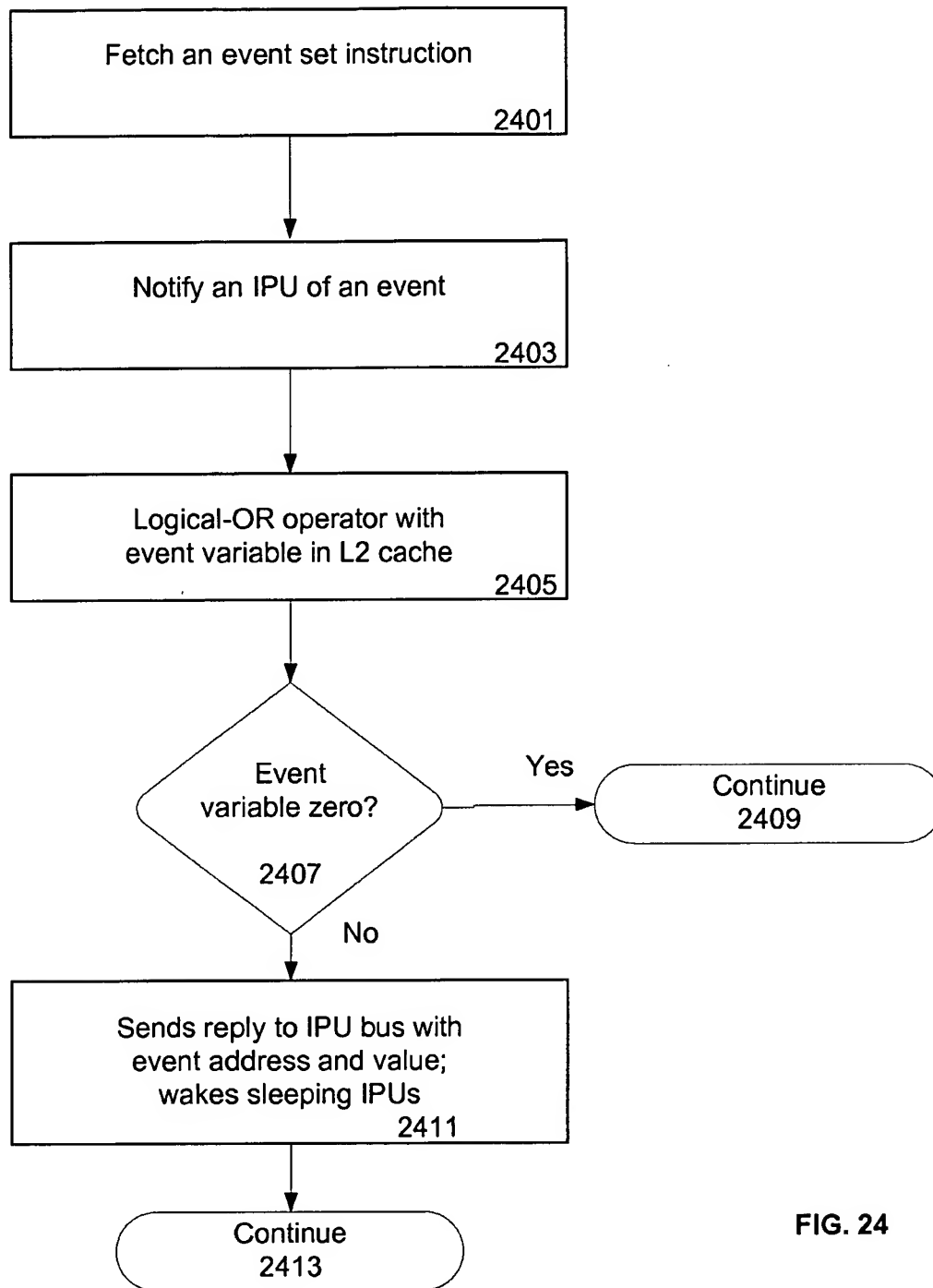


FIG. 24

## REPLACEMENT SHEET

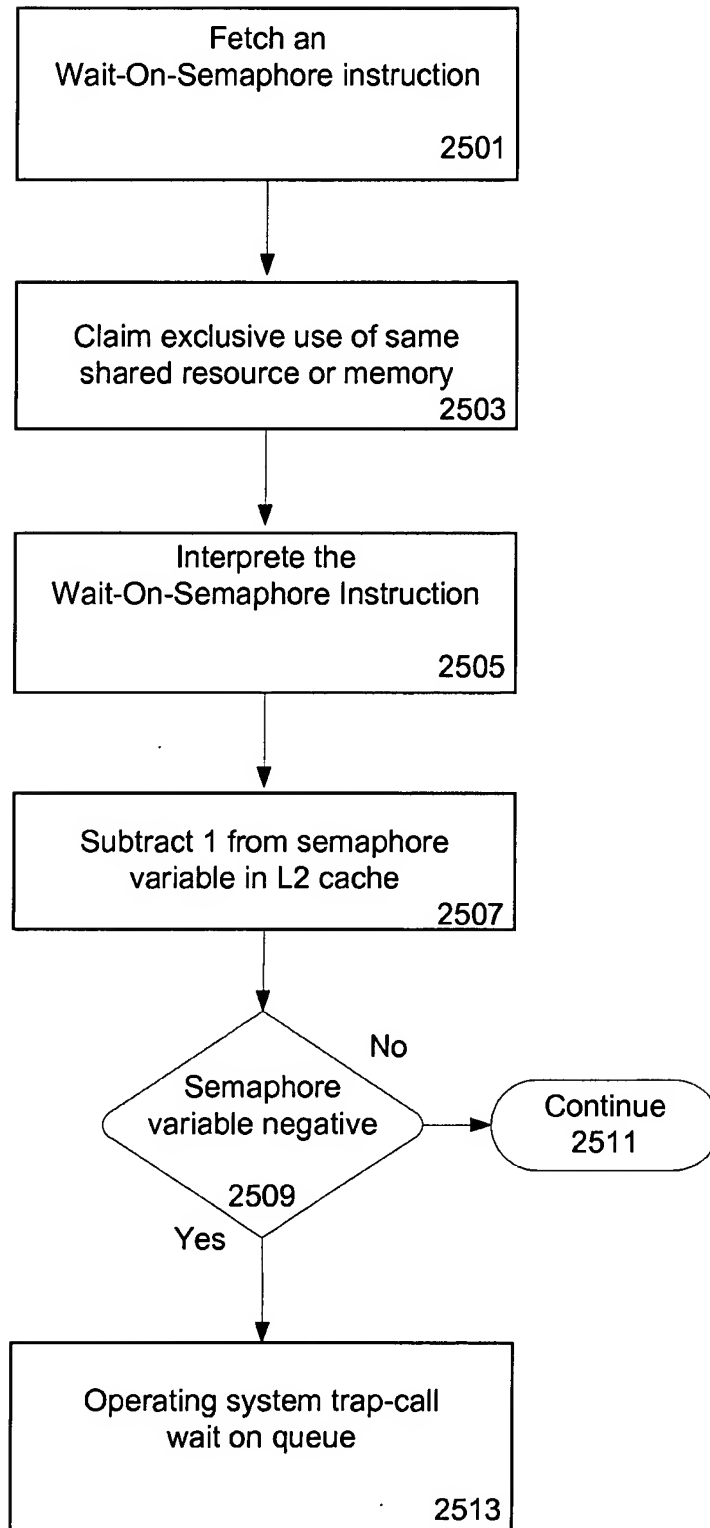


FIG. 25

# REPLACEMENT SHEET

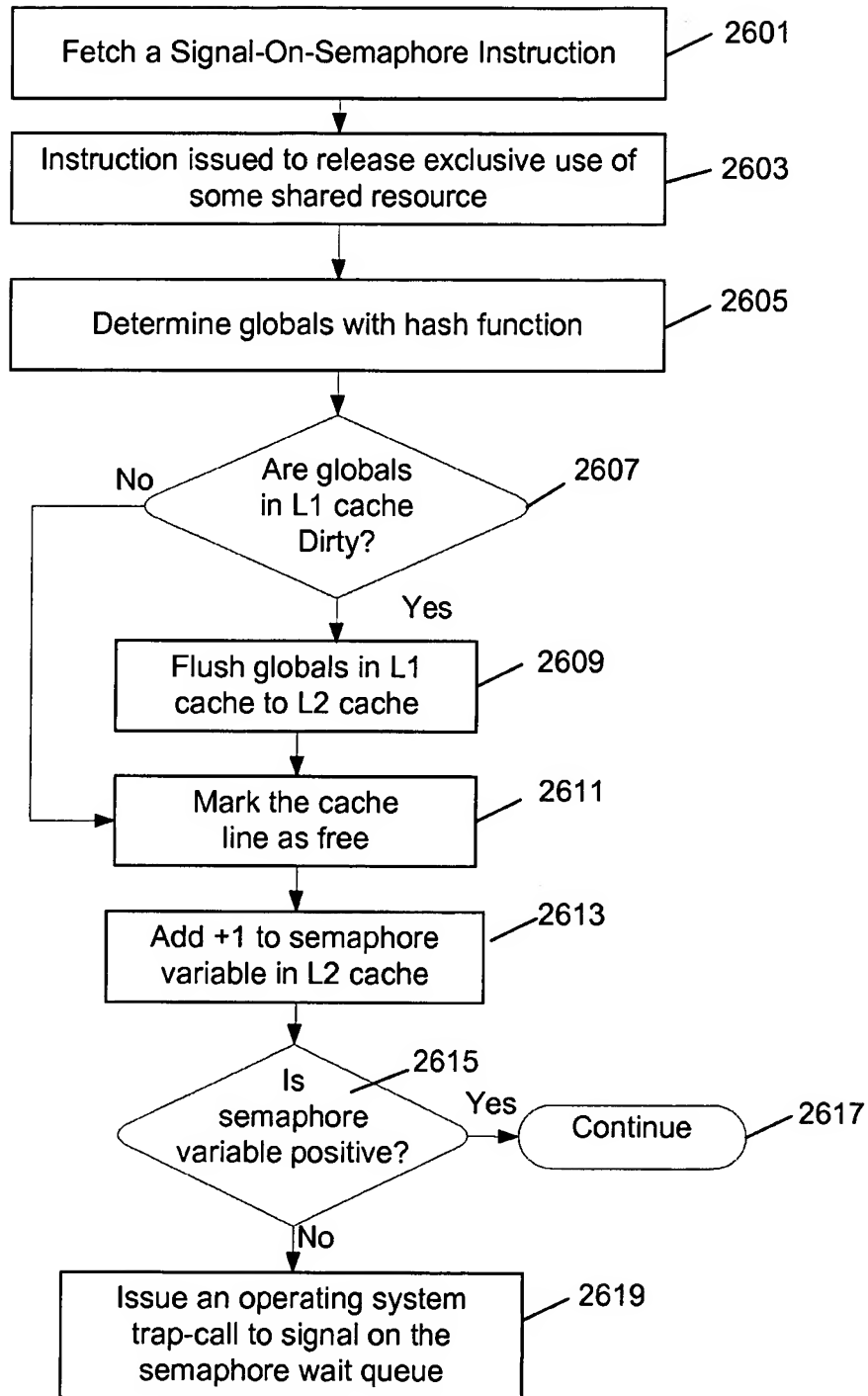


FIG. 26

# REPLACEMENT SHEET

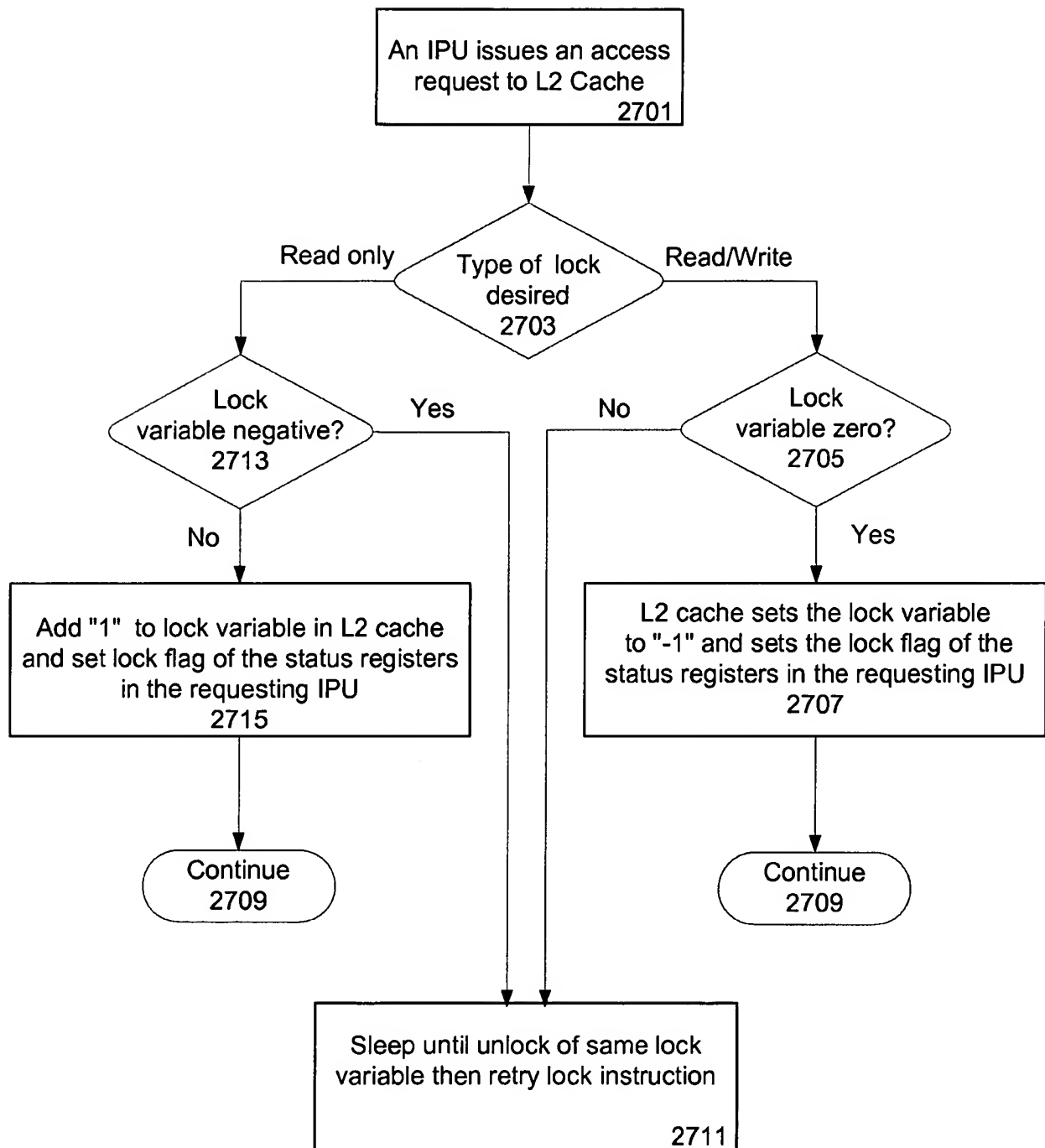
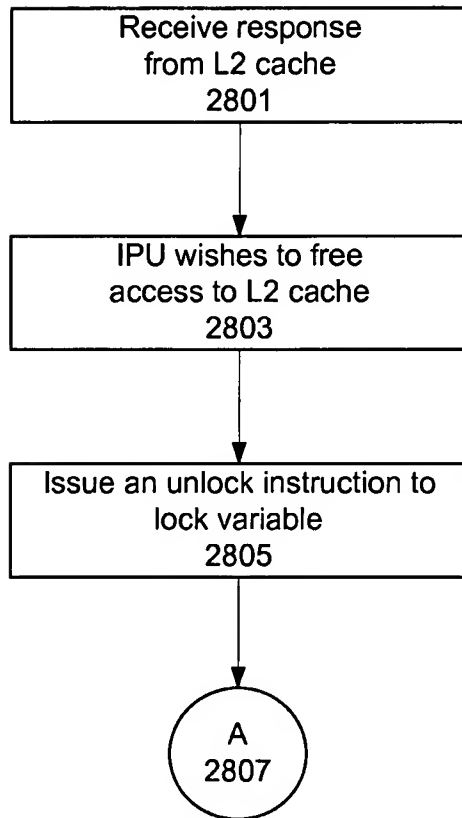


FIG. 27



## REPLACEMENT SHEET



**FIG. 28**

# REPLACEMENT SHEET

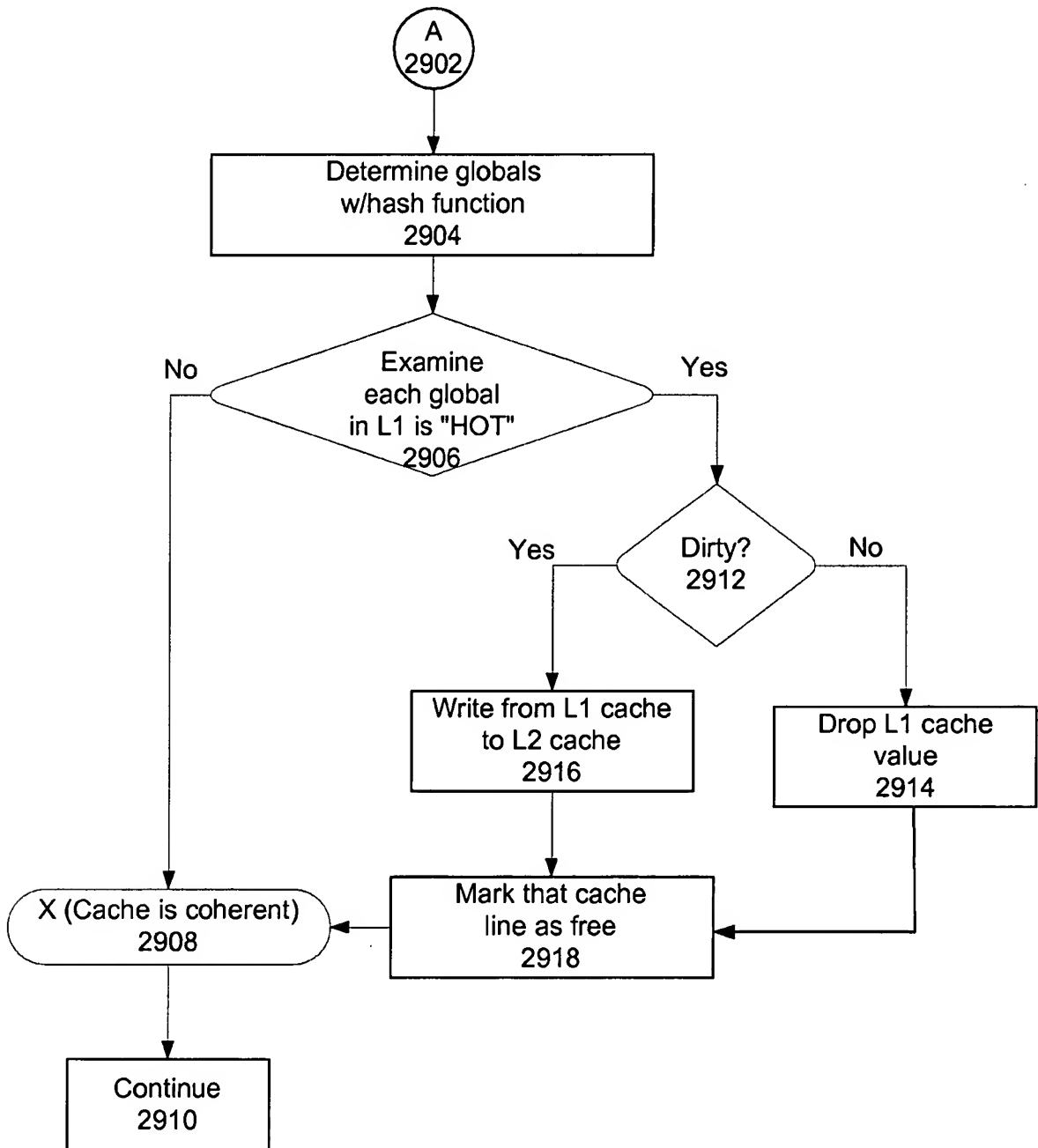


FIG. 29

REPLACEMENT SHEET

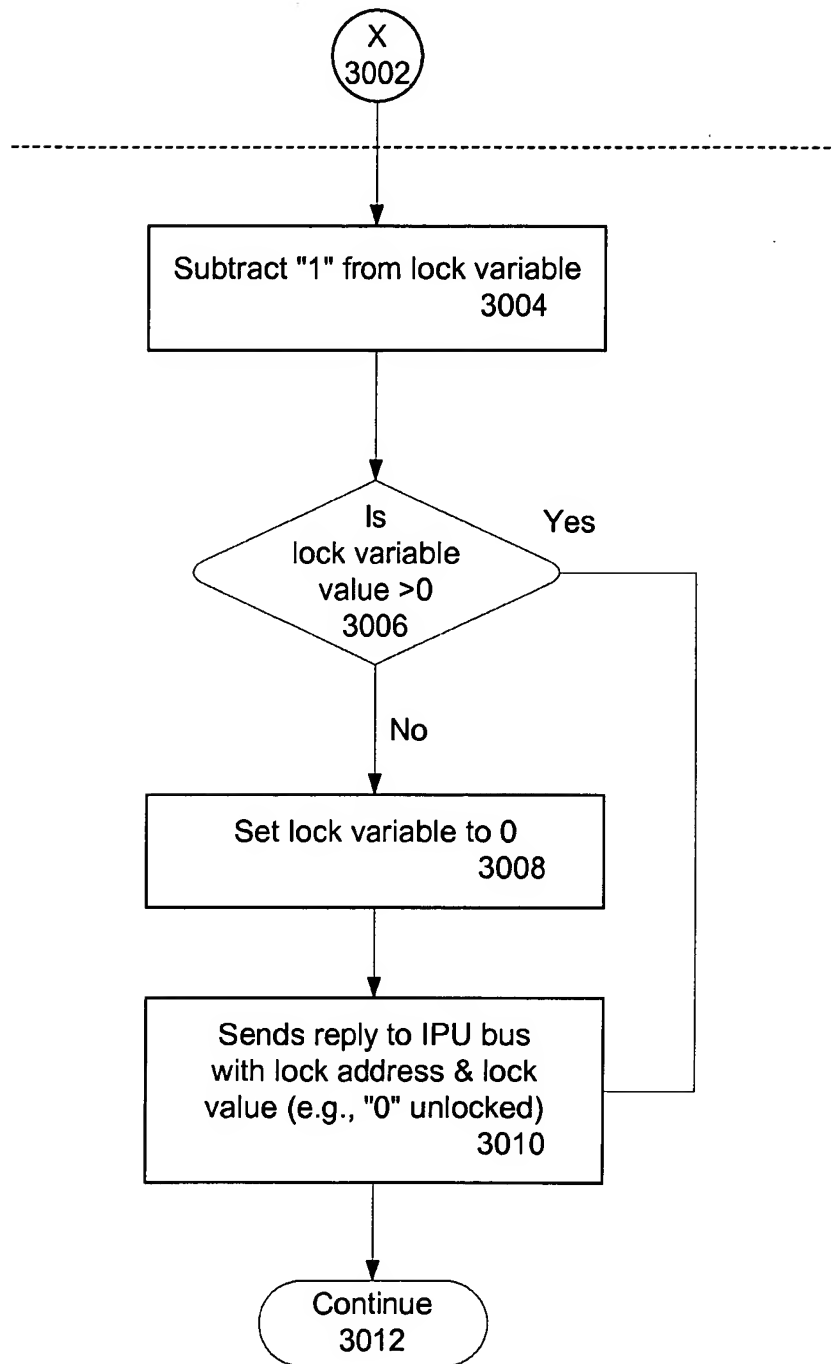
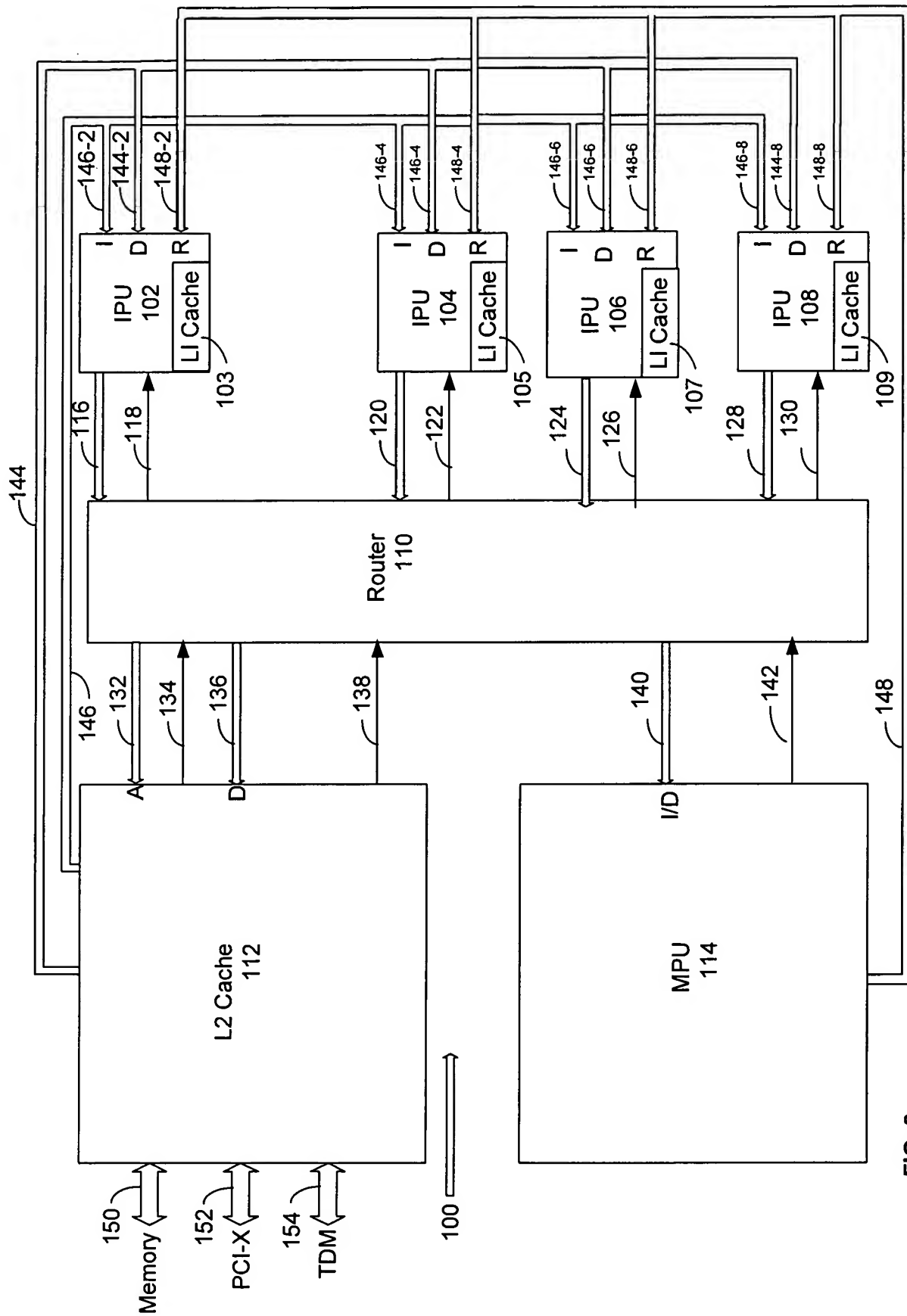
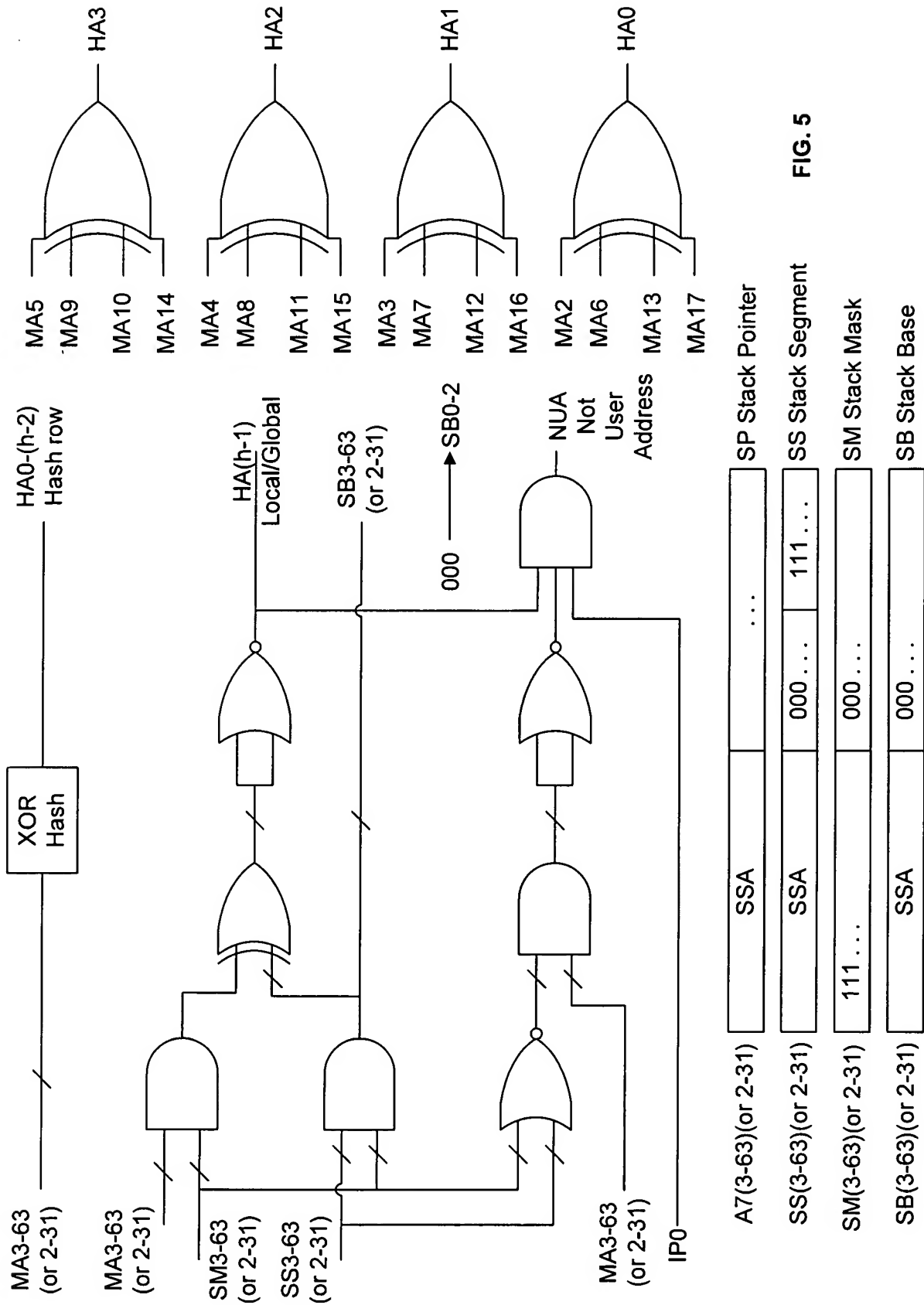


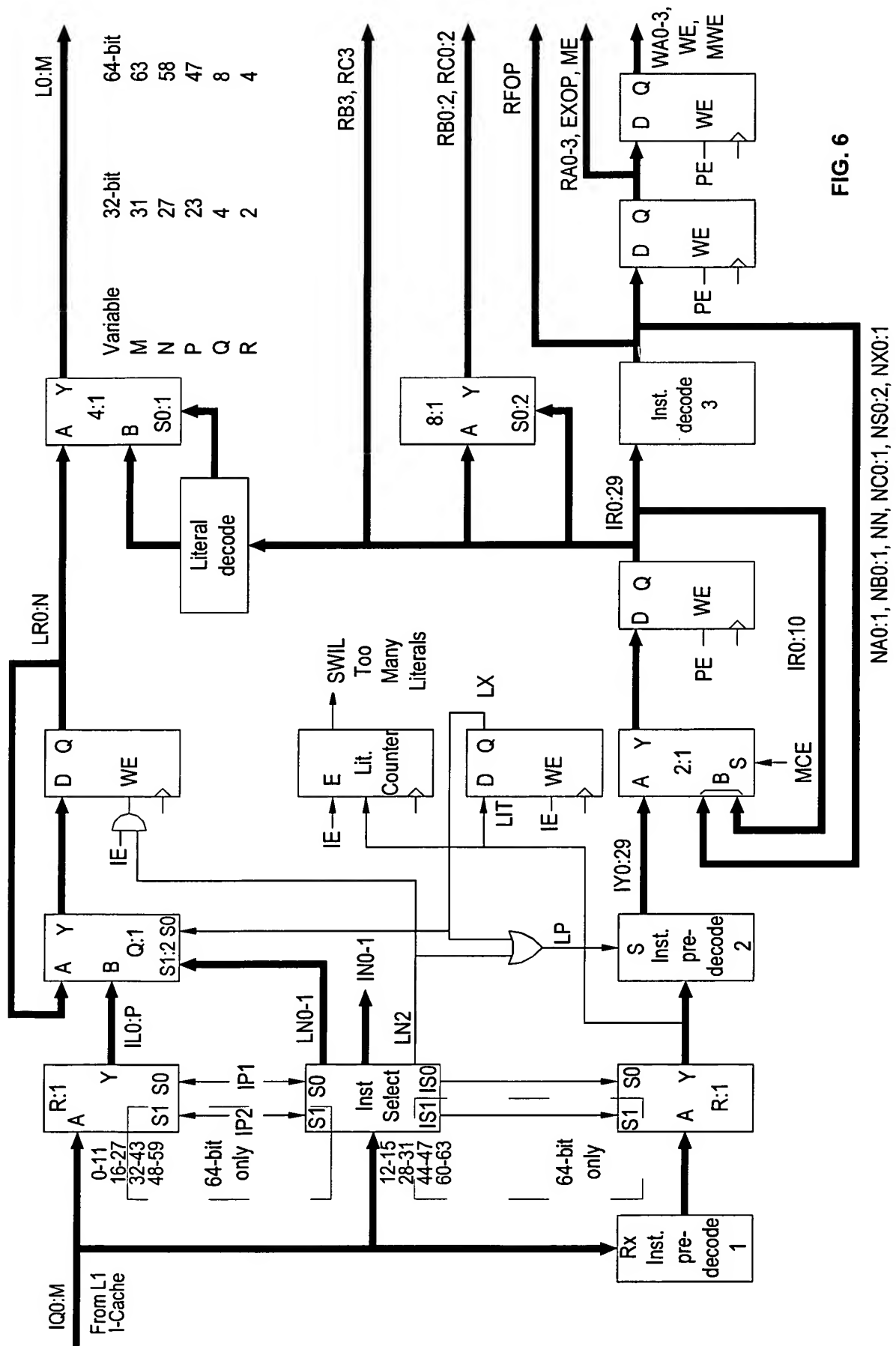
FIG. 30



**FIG. 2**

# REPLACEMENT SHEET





**FIG. 6**

NAO:1, NB0:1, NN, NC0:1, NS0:2, NX0:1

# REPLACEMENT SHEET

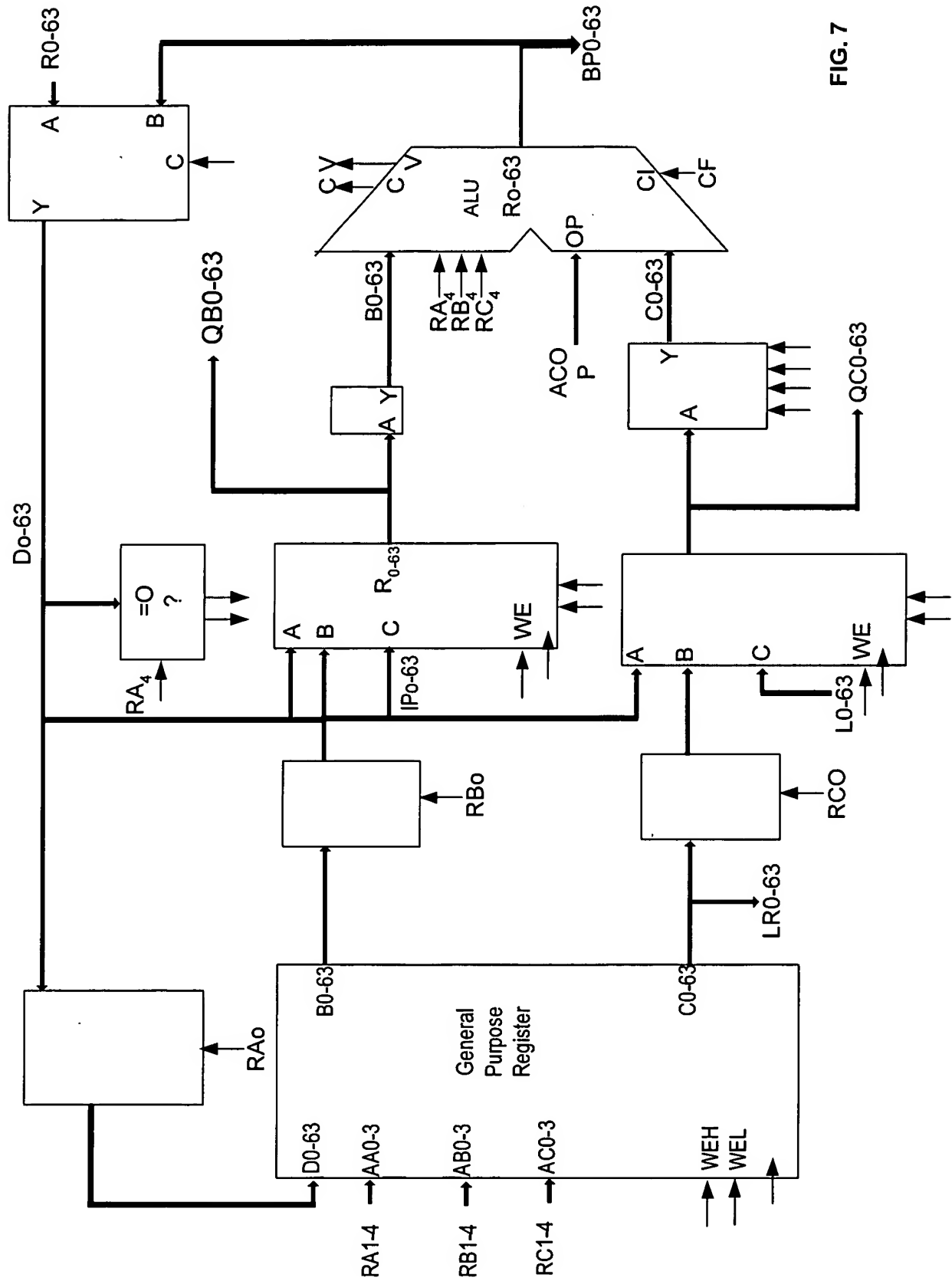
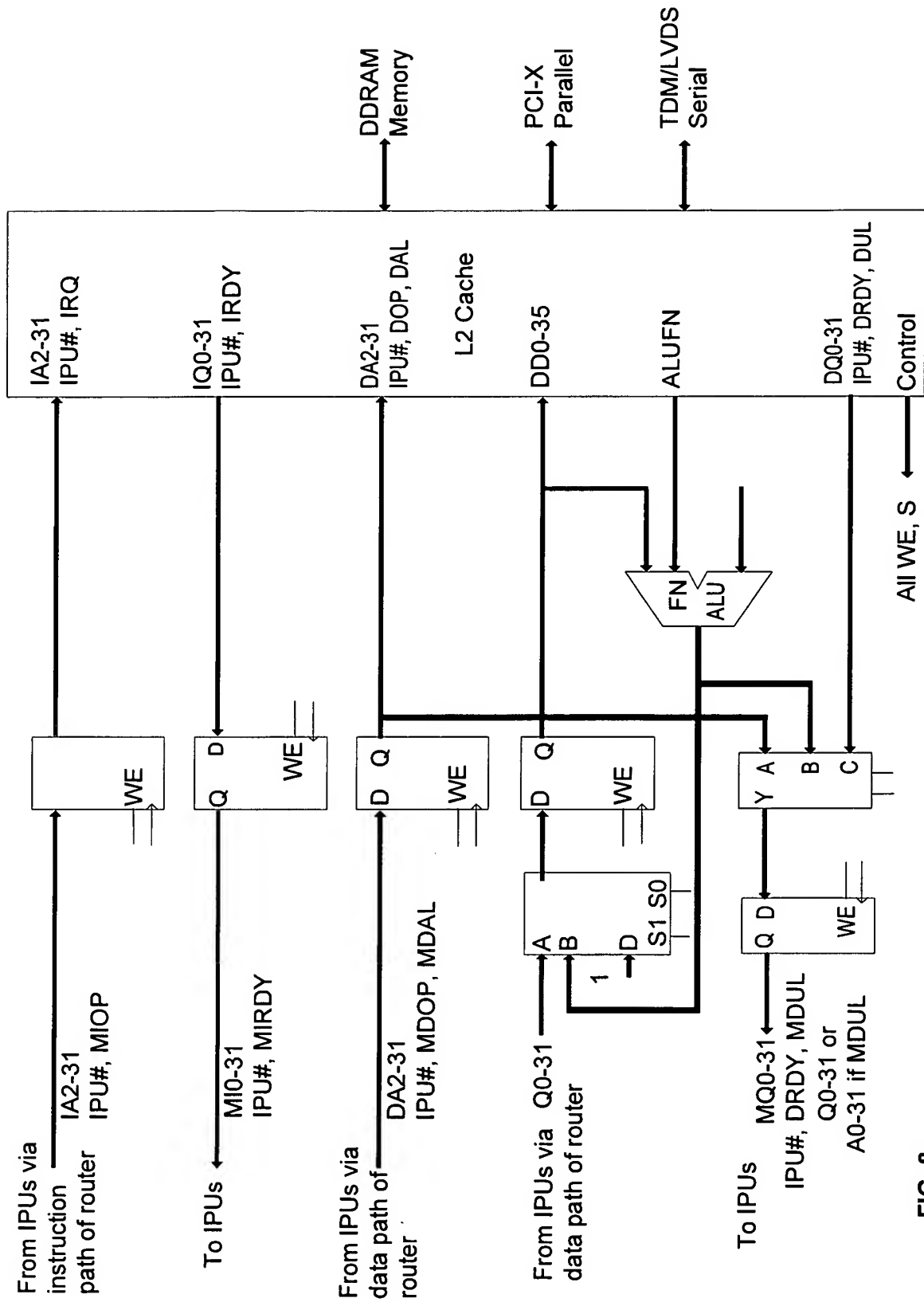


FIG. 7



**FIG. 8**